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IR-2132 (2-3575)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Thomas J. Ribarich

Date: June 22, 2005

Serial No.: 10/615,710

Group Art Unit: 2821

Filed: July 8, 2003

Examiner: Ephrem Alemu

Notice of Allowance Dated: June 6, 2005

Confirmation No. 9443

For SINGLE CHIP BALLAST CONTROL WITH POWER FACTOR CORRECTION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Attention: Official Draftsperson

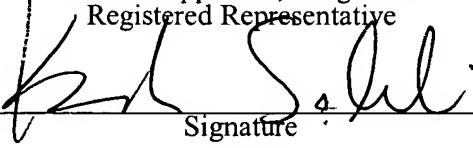
SUBMISSION OF FORMAL DRAWINGS

Sir:

Enclosed herewith please find EIGHT (8) sheets of replacement drawings containing Figures 1-13 for the above-identified application.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, Attn: Official Draftsperson, on June 22, 2005:

Kourosh Salehi

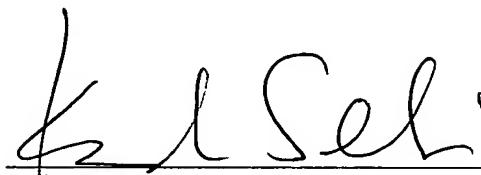
Name of applicant, assignee or
Registered Representative

Signature

June 22, 2005

Date of Signature

SHW:fs
Enclosure

Respectfully submitted,



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